

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Lefurgy, et al. DOCKET NO.: AUS920030894US1
SERIAL NO: (Not Yet Assigned) EXAMINER: (Not Yet Assigned)
FILED: (herewith) ART UNIT: (Not Yet Assigned)
TITLE: METHOD AND SYSTEM FOR POWER MANAGEMENT INCLUDING LOCAL
BOUNDING OF DEVICE GROUP POWER CONSUMPTION

MAIL STOP PATENT APPLICATION
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Weiss, Moy & Harris, P.C.
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Dear Sir:

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This Information Disclosure Statement is submitted in regards to the above-identified patent application. A PTO-SB/08 form is attached along with copies of the references cited therein.

No fee is believed to be required in connection with this Information Disclosure Statement. However, if there are any fees incurred by this transmittal, please deduct them from IBM Deposit Account NO. 09-0447.

Respectfully submitted,



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of

2

Complete if Known

Application Number

Filing Date

First Named Inventor

Lefurav

Art Unit

Examiner Name

Attorney Docket Number

AUS920030894US1

U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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Complete if Known**Application Number****Filing Date****First Named Inventor**

Lefurgy

Art Unit**Examiner Name**

Sheet

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of

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Mizumo, et al., "Chip OS: Open Power-management Platform to Overcome the Power Crisis in Future LSIs" IEEE ISSC Conference, 2001, USA	
		Gunther, et al., "Managing the Impact of Increasing Microprocessor Power Consumption", Intel, 2001, USA, web publication at http://developer.intel.com	

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